

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Anand Murthy, et al.

Application No.: 09/475,452

Filed: 12/30/1999

For: A NOVEL MOS TRANSISTOR
STRUCTURE AND METHOD OF
FABRICATION

Examiner: Lee, Eugene

Art Unit: 2815

Confirmation No. 6341

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Commissioner for Patents
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Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed).

Applicant appreciates that, pursuant to 37 C.F.R. § 1.97(i), the Examiner need not consider this Information Disclosure Statement. At a minimum, Applicant respectfully requests that the Examiner place this letter and the enclosures in the USPTO file for the present application.

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

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Respectfully submitted,

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Dated: May 13, 2008

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